

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Takashi YOSHITOMI et al.

Serial No.

10/619,394

Filing Date

July 15, 2003

For

SEMICONDUCTOR DEVICE WITH CAPACITOR AND

PROCESS FOR MANUFACTURING THE DEVICE

Examiner

Not Yet Assigned

Group Art Unit

2811

745 Fifth Avenue

New York, New York 10151

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" Service under 37 CFR 1.10 on the date indicated above and is addressed to: Mail Stop Patent Application, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Further to the Information Disclosure Statement filed on July 15, 2003, the Examiner's attention is respectfully invited to review the accompanying form PTO-1449 along with copies of the references identified therein.

FOREIGN PATENT

1. Japanese Patent Laid Open No. 8-111377, Published: April 30, 1996.

This Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3) and is not a representation that the document cited herein is considered most pertinent, or that a search has been undertaken, or that the cited document is indeed prior art. The Examiner is respectfully invited to undertake an independent search.

Applicants respectfully request that the Examiner consider and make of record the document cited herein and that a copy of Form PTO-1499, appropriately initialed by the Examiner be returned to Applicants' attorney.

It is believed that no fee is due, however, the Commissioner is hereby authorized to charge any fee or credit any overpayment therein to Deposit Account No. 50-0320.

Respectfully submitted,

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Based on Form PTO-1449					ATTY. DOCKET NO.		Sheet 1 of 1 SERIAL NO.		
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